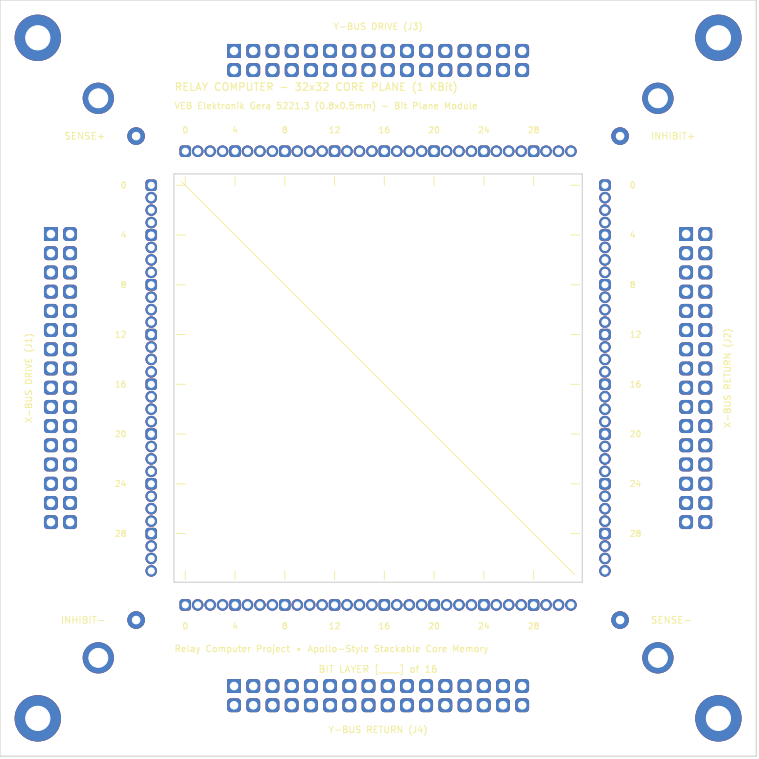

Y-BUS DRIVE (J3)
RELAY COMPUTER — 32x32 CORE PLANE (1 KBit)
VEB Elektronik Gera 5221.3 (0.8x0.5mm) — Bit Plane Module
SENSE+
INHIBIT+
0 4 8 12 16 20 24 28
X-BUS DRIVE (J1)
X-BUS RETURN (J2)
INHIBIT−
SENSE−
Relay Computer Project • Apollo-Style Stackable Core Memory
BIT LAYER [__] of 16
Y-BUS RETURN (J4)